

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

## T78041

### LINEAR INTEGRATED CIRCUIT

# VERTICAL DEFLECTION **OUTPUT CIRCUIT**

#### DESCRIPTION

The UTC T78041 is a monolithic integrated circuit and designed for use in high-definition TV and CRT monitors. It is intended to directly drive the deflection coil. Besides, the T78041 offers a maximum deflection current of 2.2A peak to peak to suitable for large diameter CRTs.

#### **FEATURES**

- \* Deflection current can be 2.2A peak value
- \* Deflection voltage up to 70V
- \* Flyback Generator
- \* Thermal Protection Circuit
- \* Low cross-over distortion
- \* Supports DC Coupling

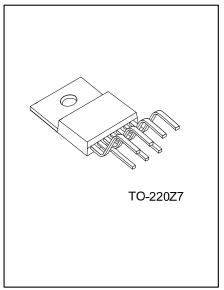
#### **APPLICATIONS**

\* Vertical deflection for monitors and TVs

### ODEDING INFORMATION

ORDERING INFO	DRIMATION			
Order Number		Dookago	Decking	
Normal	Lead Free Plating	Package	Packing	
T78041-TB7-T	T78041L-TB7-T	TO-220Z7	Tube	

T78041L-TB7-T (1)Packing Type (2)Package Type (3)Lead Plating	(1) T: Tube (2) TB7: TO-220Z7 (3) Lead Free Plating, Blank: Pb/Sn
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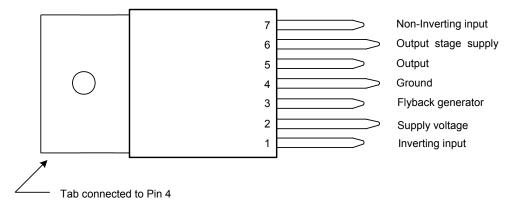


\*Pb-free plating product number: T78041L

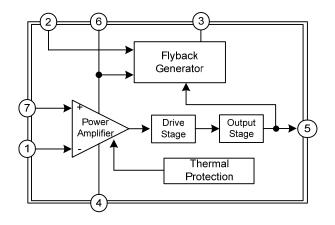
# T78041

### LINEAR INTEGRATED CIRCUIT

### ■ PIN CONFIGURATIONS



### BLOCK DIAGRAM





### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage (pin 2 to Pin4)	Vcc2	34	V
Output Supply Voltage (pin 6 to Pin4)	Vcc6	70	V
Output Peak Current	I <sub>5MAX</sub>	-1.5 ~ +1.5	А
Power Dissipation	PD	9	W
Junction Temperature	TJ	150	°C
Operating Temperature	T <sub>OPR</sub>	-20 ~ +85	°C
Storage Temperature	T <sub>STG</sub>	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### THERAML DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Thermal Resistance Junction-Case	θ <sub>JC</sub>	3.0	°C/W

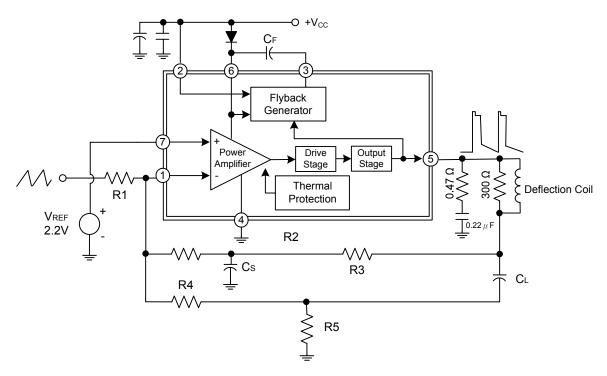
### ■ ELECTRICAL CHARACTERISTICS (Ta=25°C, Vcc=24V, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V <sub>CC</sub>		16	24	33	V
Quiescent Current	lq		35	-	65	mA
Recommend Biggest Deflect Current	I <sub>5P-P</sub>				2.2	А
Output Saturated Voltage to GND	$V_{5L}$	I <sub>5</sub> =1.1A			1.5	V
Output Saturated Voltage to Supply	V <sub>5H</sub>	I₅=-1.1A			3.5	V
Pin 3 Saturation Voltage to GND	V <sub>3L</sub>	I <sub>3</sub> =20mA			1.8	V
Pin 3 Saturation Voltage to GND (Return to Sweep the Second Part)	V <sub>3(2)</sub>	I <sub>3</sub> =-1.1A			3.2	V
Output Middle Point Voltage	Vo(MID)		11	12	13	V
Thermal Shutdown Temperature				140		°C

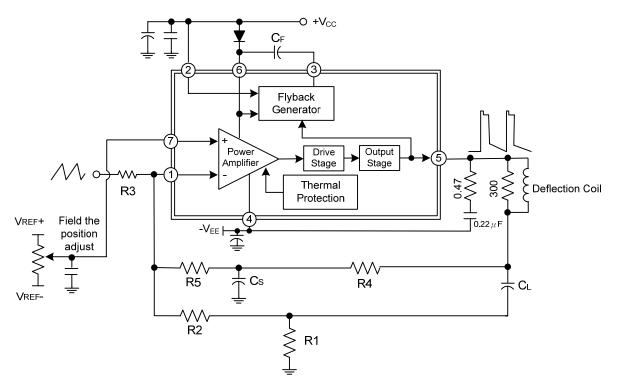


### APPLICATION CIRCUIT

For AC Coupling (Single Power Supply)



For DC Coupling (Dual Power Supply)





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